

PATENT



DOCKET NO.: Intel 2207/6039  
ASSIGNEE: Intel Corporation

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1-6-04  
P.2.

Response Under 37 C.F.R. §1.116  
Expediting Procedure  
Examining Group 2613

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS : Liuyang YANG  
SERIAL NO. : 09/116,147  
FILED : July 16, 1998  
FOR : METHOD AND APPARATUS TO IMPROVE  
EFFICIENCY IN MULTIPLE-PASS, BIT-RATE-  
CONTROLLED FRAME ENCODING  
GROUP ART UNIT : 2613  
EXAMINER : Y. Young LEE

**RECEIVED**

DEC 31 2003

Technology Center 2600

M/S: AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express mail and deposited with the U.S. Postal Service and addressed to:  
M/S: AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

Dated: December 23, 2003

Signature

*Stephen T. Neal*  
Stephen T. Neal

**AMENDMENT UNDER 37 C.F.R. §1.116 AND**  
**REQUEST FOR RECONSIDERATION**

Dear Sir:

The following were sent in response to the office action mailed on September 24, 2003.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2  
of this paper.

**Remarks/Arguments** begin on page 10 of this paper.